

In the Specification

Please replace the paragraph beginning at line 15 on page 8 with the following marked up paragraph:

Thus, the cross-connect 120 of figure 1 uses as its storage the store registers of each matrix entry of each matrix. Whereas the prior ~~are~~ art had separate TSI and switch units with their own storage to reorder, intermix, and store, the storage of the cross-connect 120 is shared for these various operations. Thus, the cross-connect 120 requires less storage than the prior art cross-connect. In addition, since the cross-connect 120 can be implemented with register type storage and since register type storage can operate at higher frequencies, the cross-connect can be implemented at a greater density.